

FIG. 1

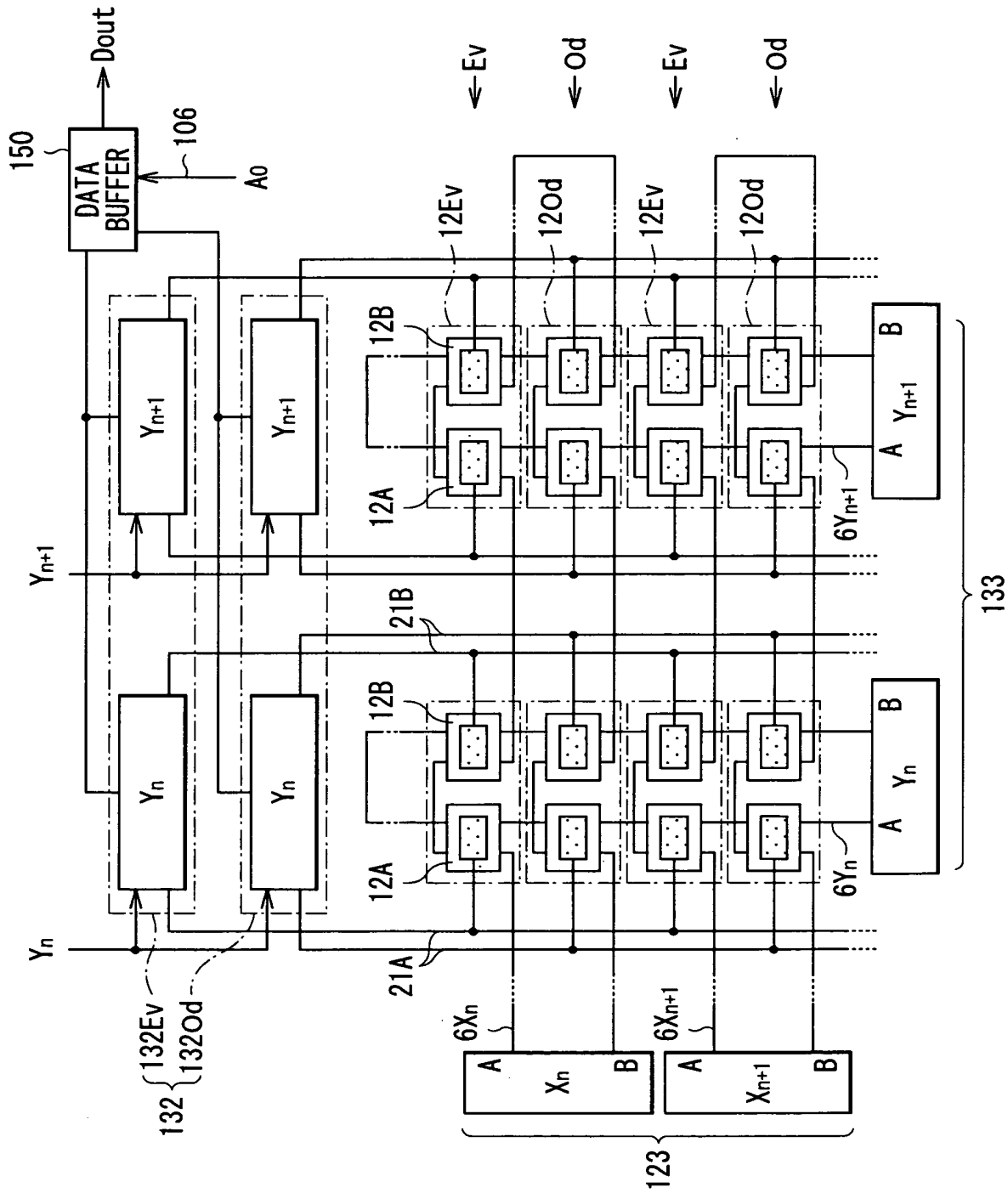


FIG. 2

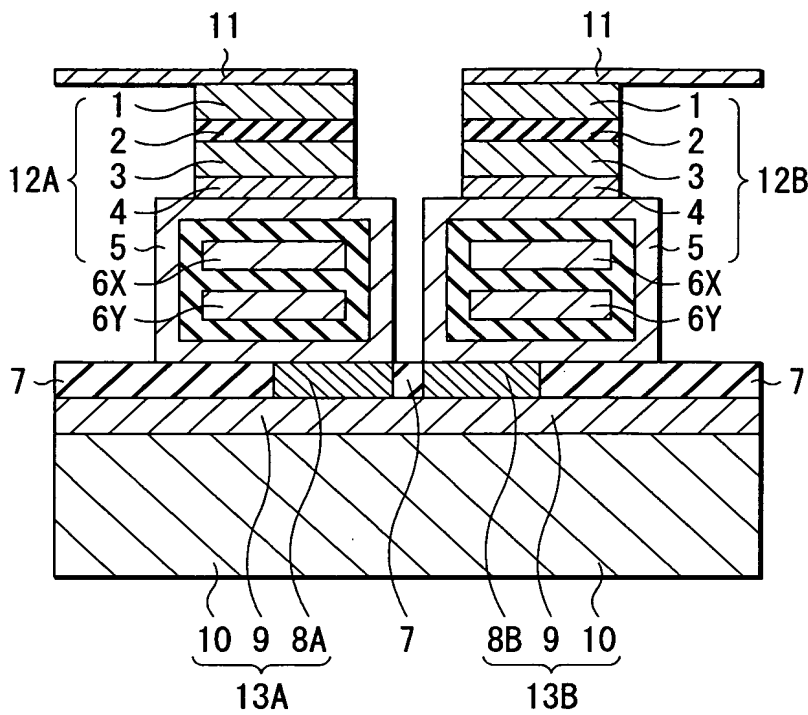


FIG. 3

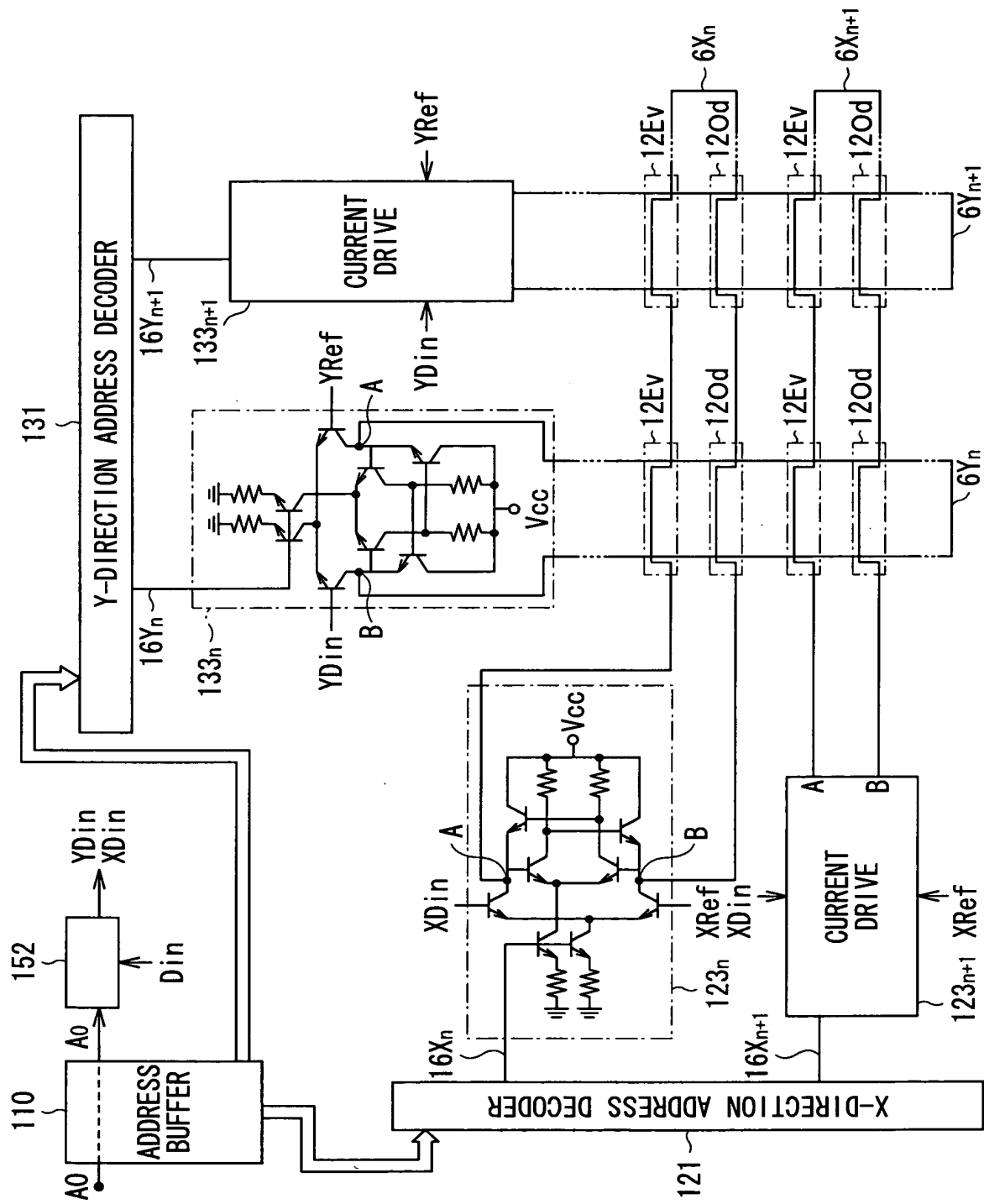
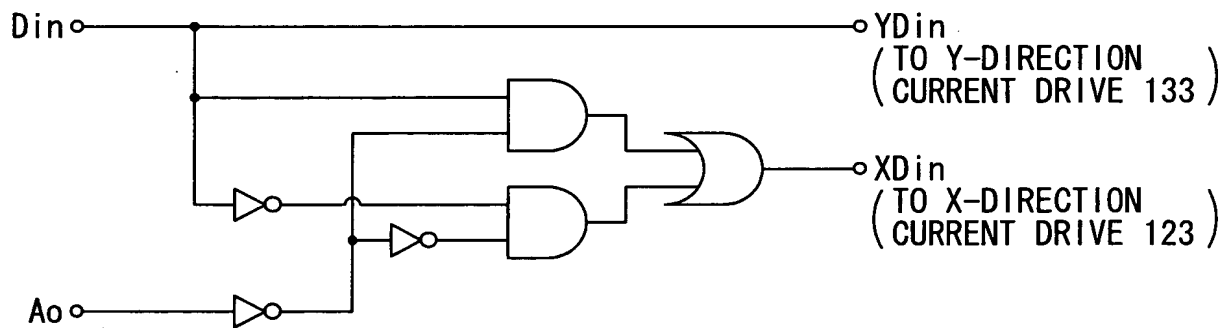


FIG. 4

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	A <sub>0</sub>	D <sub>in</sub>	X <sub>Din</sub>	Y <sub>Din</sub>
WRITE "1" TO MEMORY CELL 12Ev	0	1	1	1
WRITE "0" TO MEMORY CELL 12Ev	0	0	0	0
WRITE "1" TO MEMORY CELL 12Od	1	1	0	1
WRITE "0" TO MEMORY CELL 12Od	1	0	1	0

FIG. 5



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FIG. 6

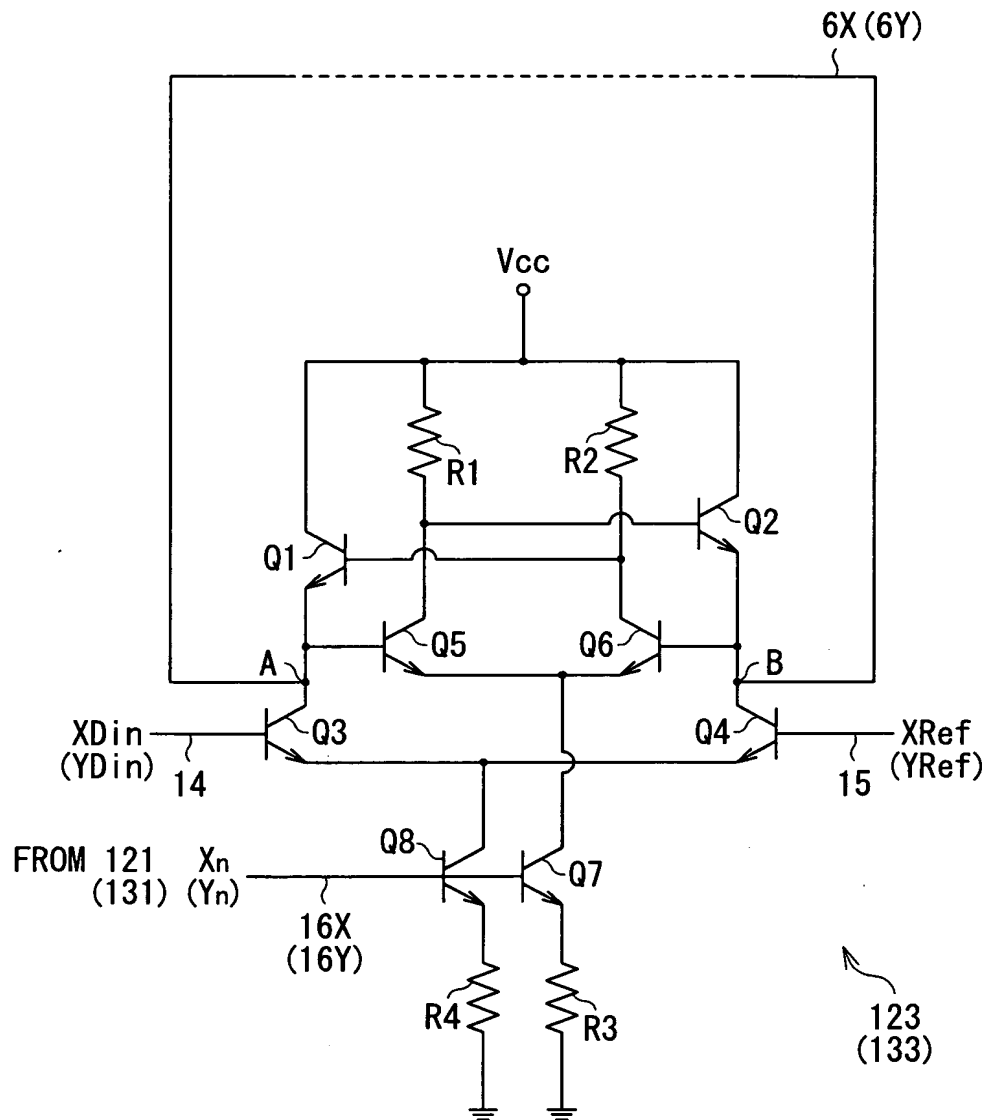


FIG. 7

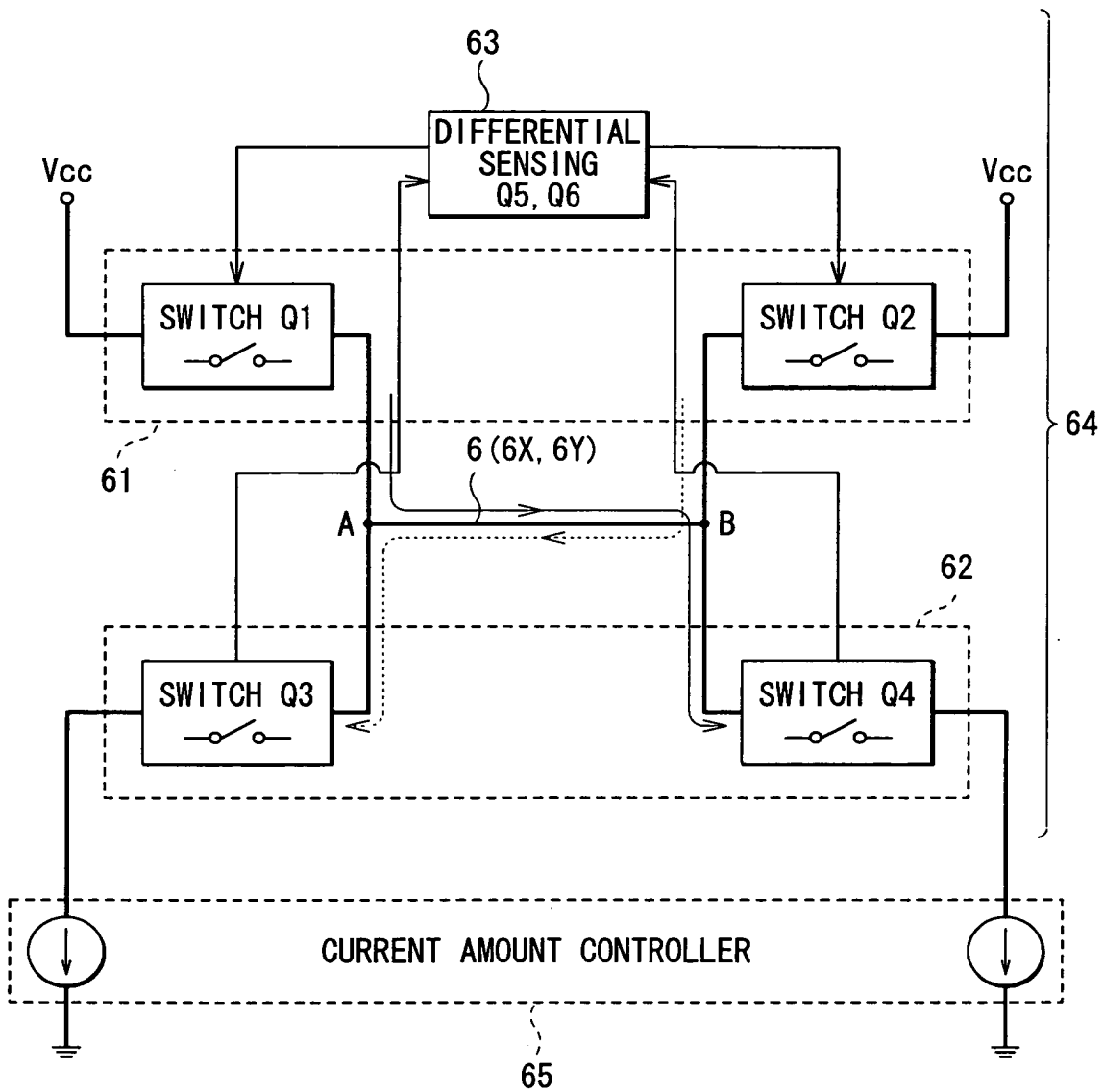


FIG. 8

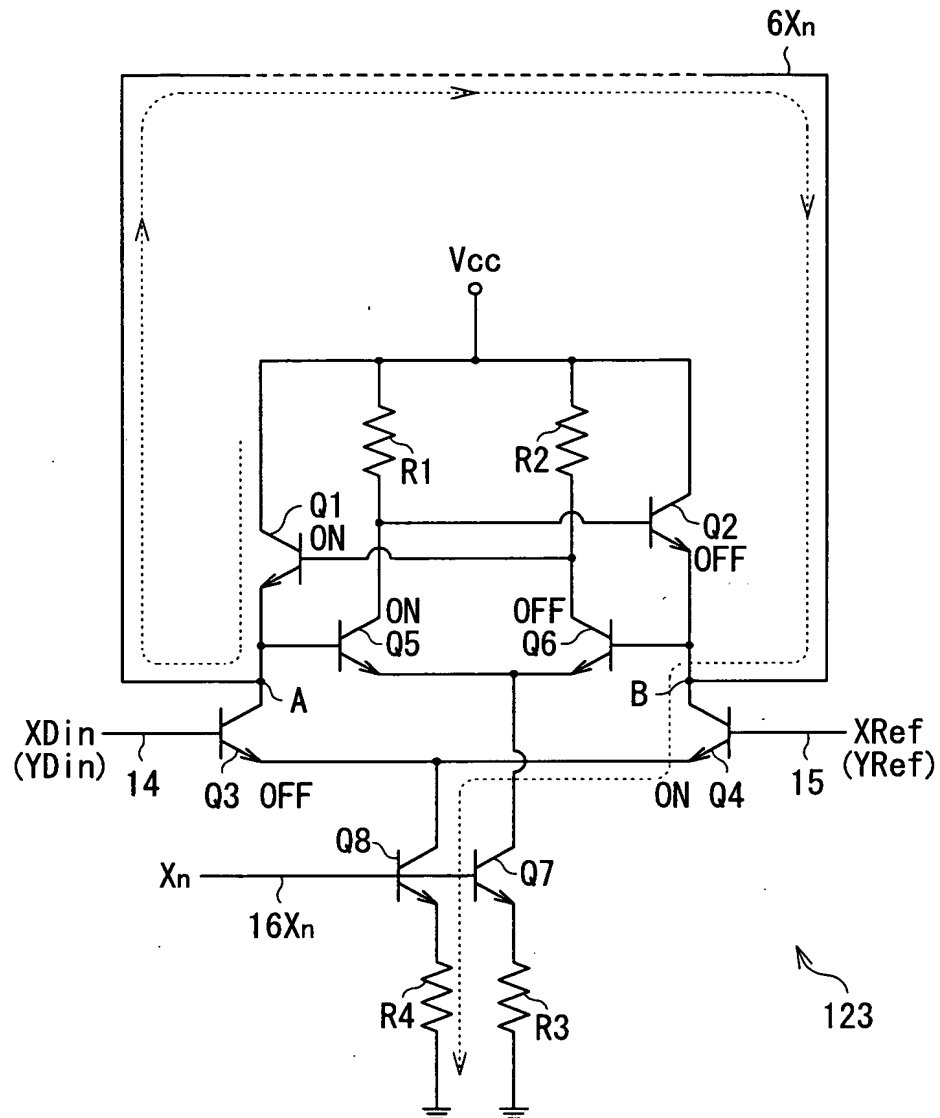


FIG. 9



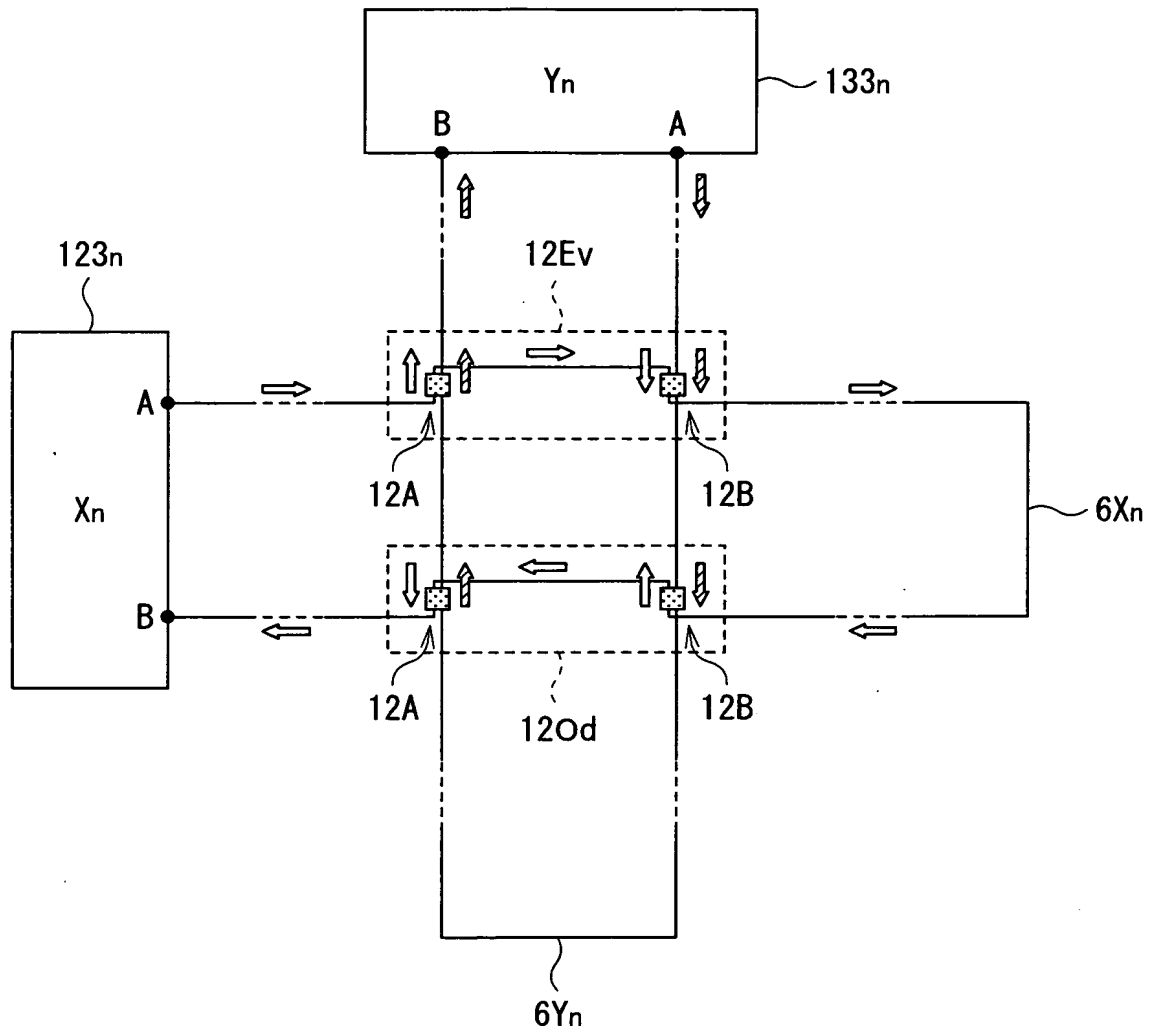


FIG. 10

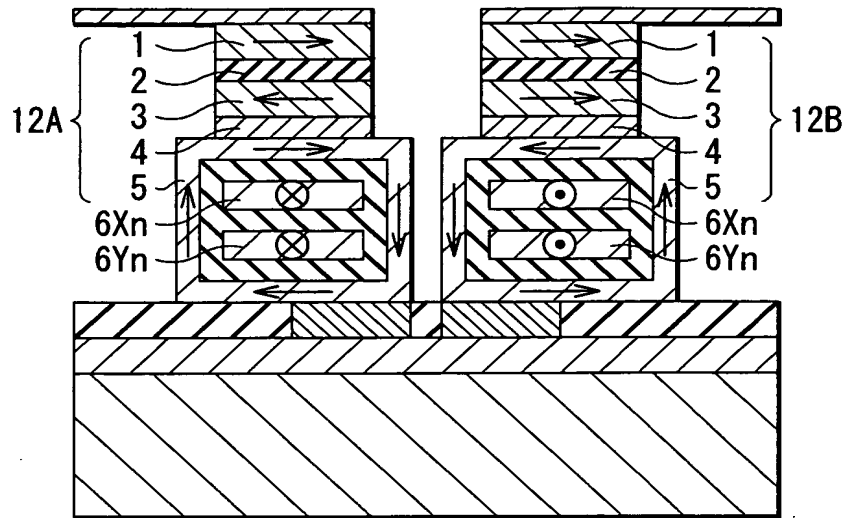


FIG. 11

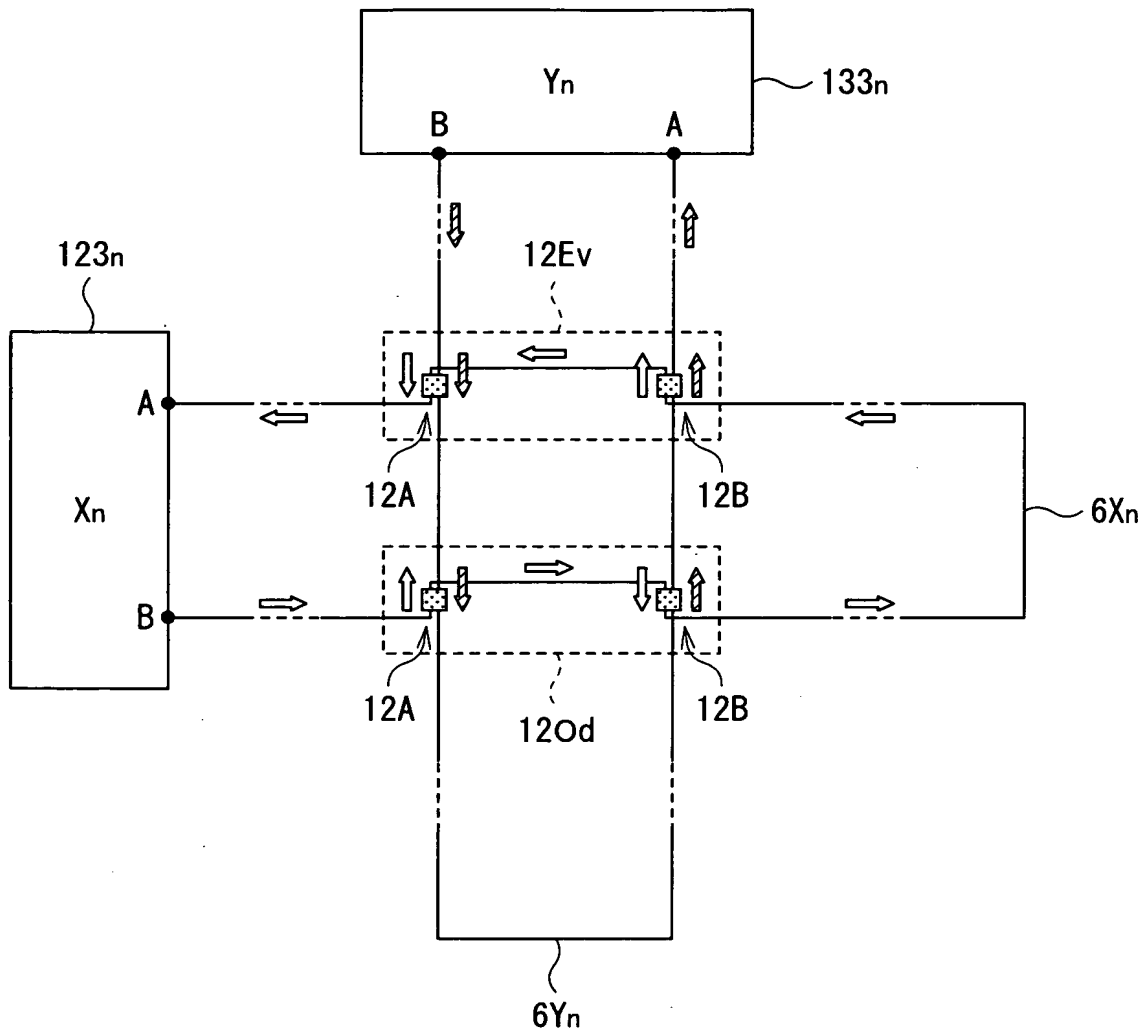


FIG. 12

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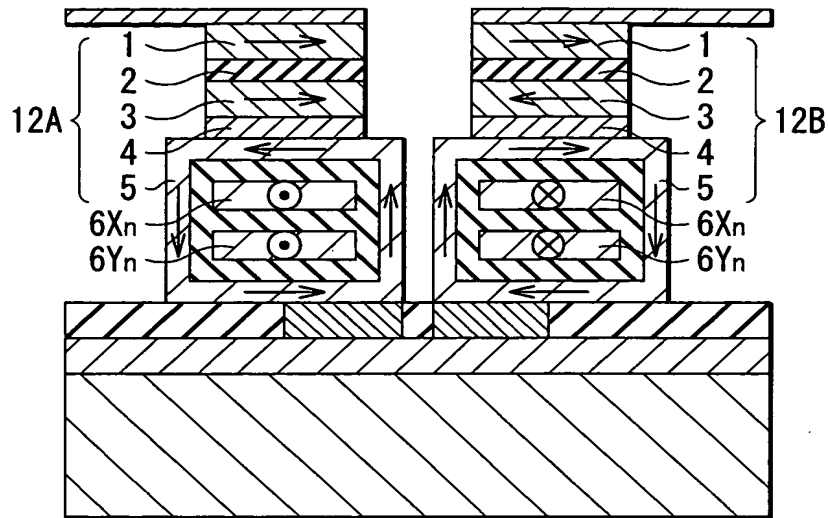


FIG. 13

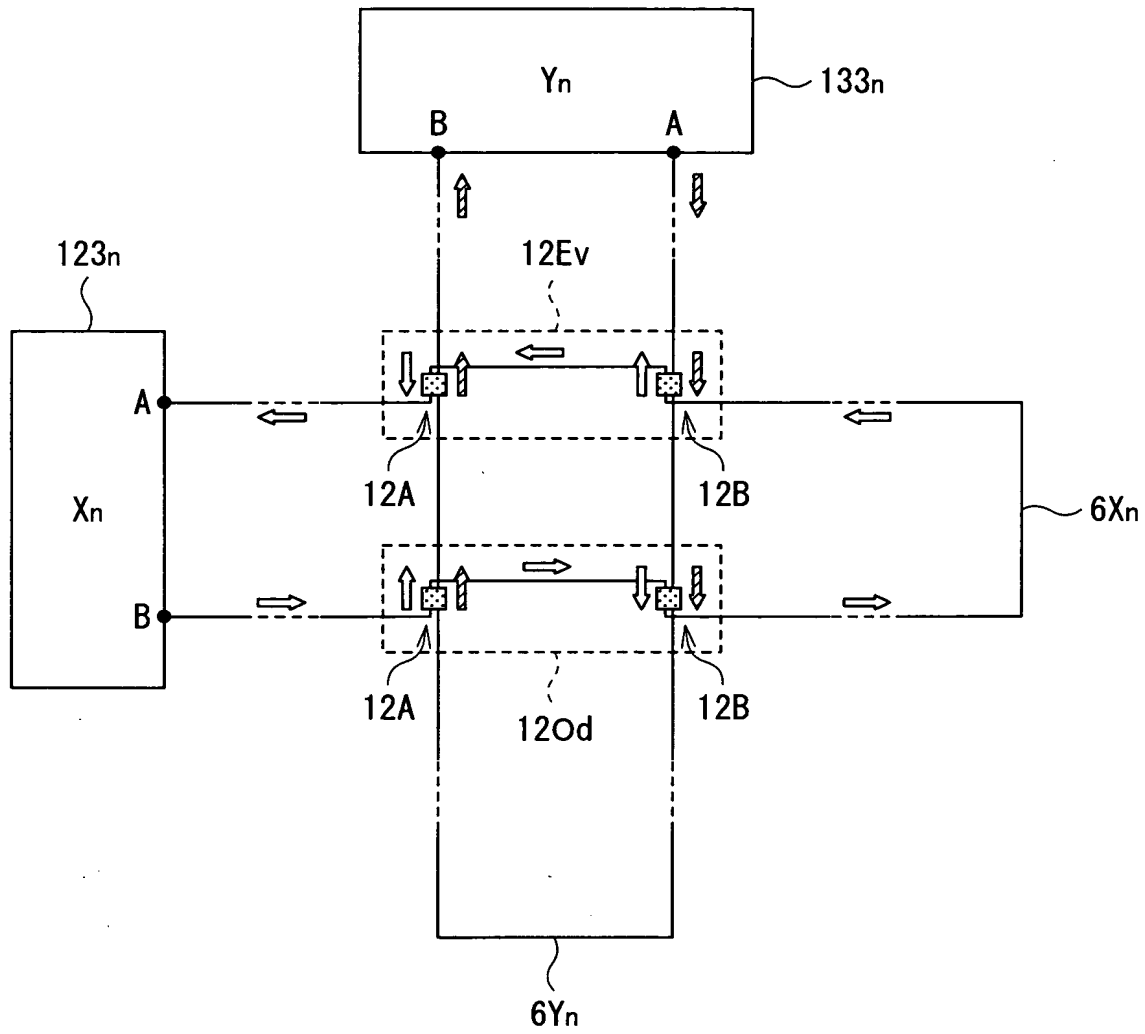


FIG. 14

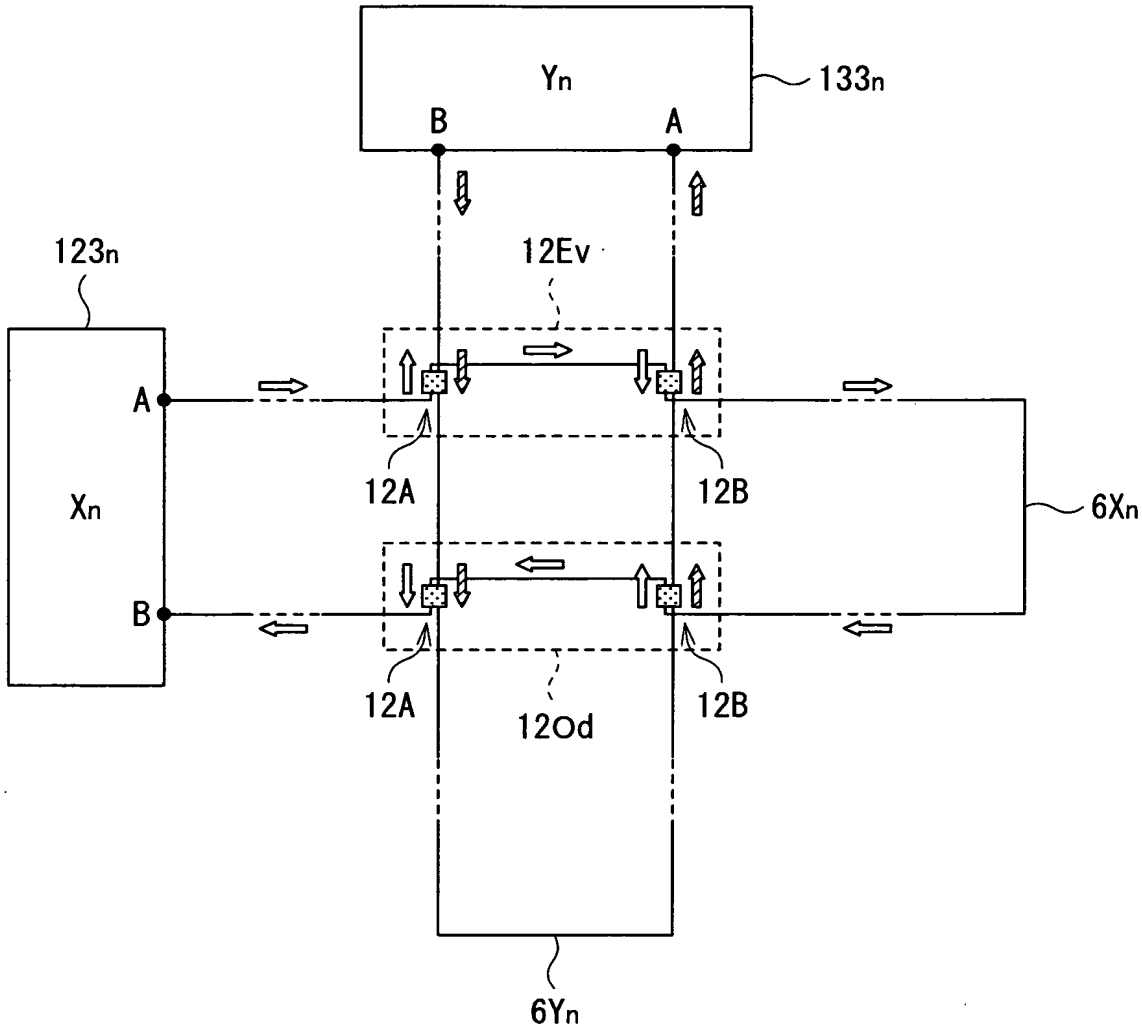


FIG. 15

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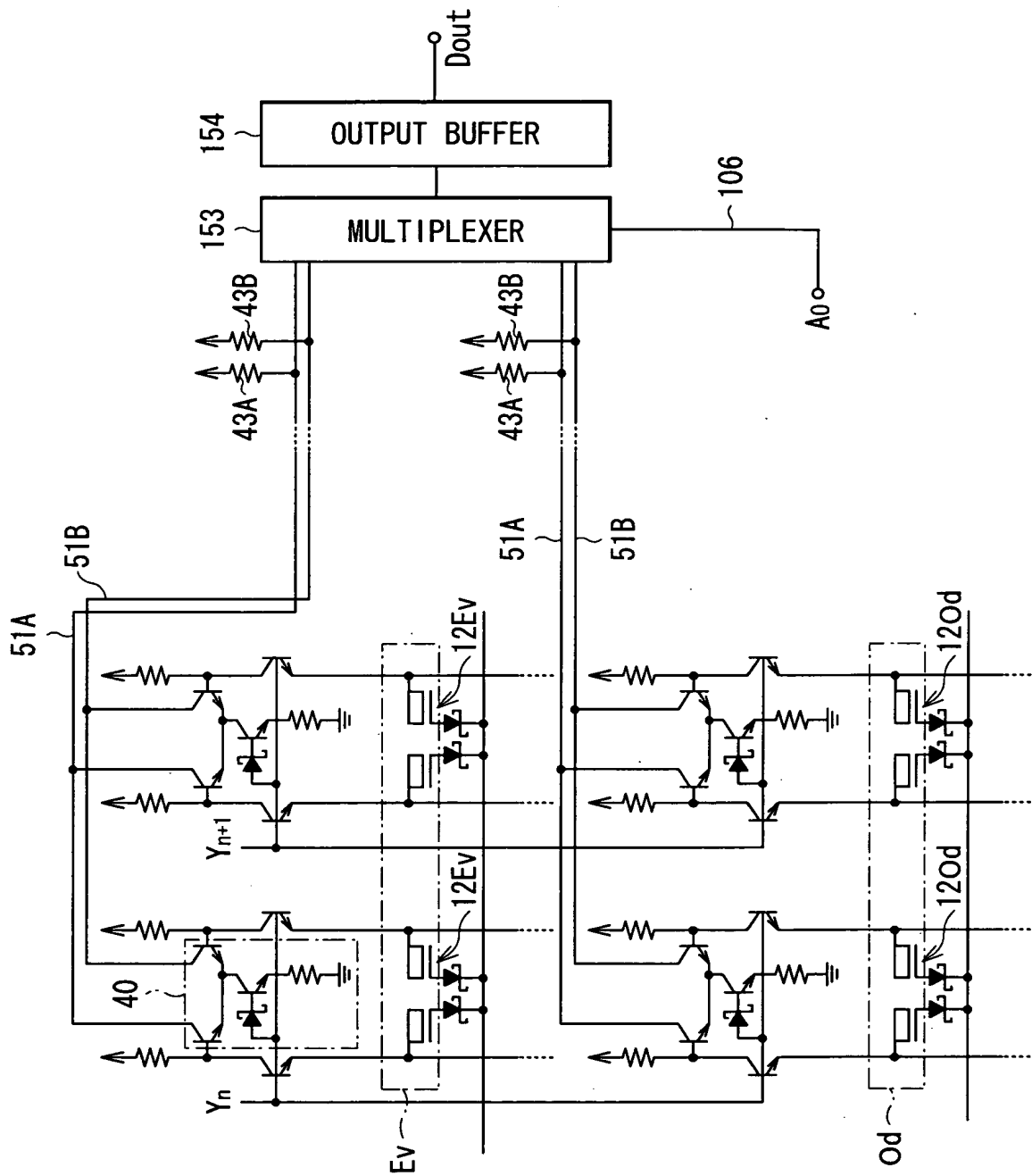


FIG. 16

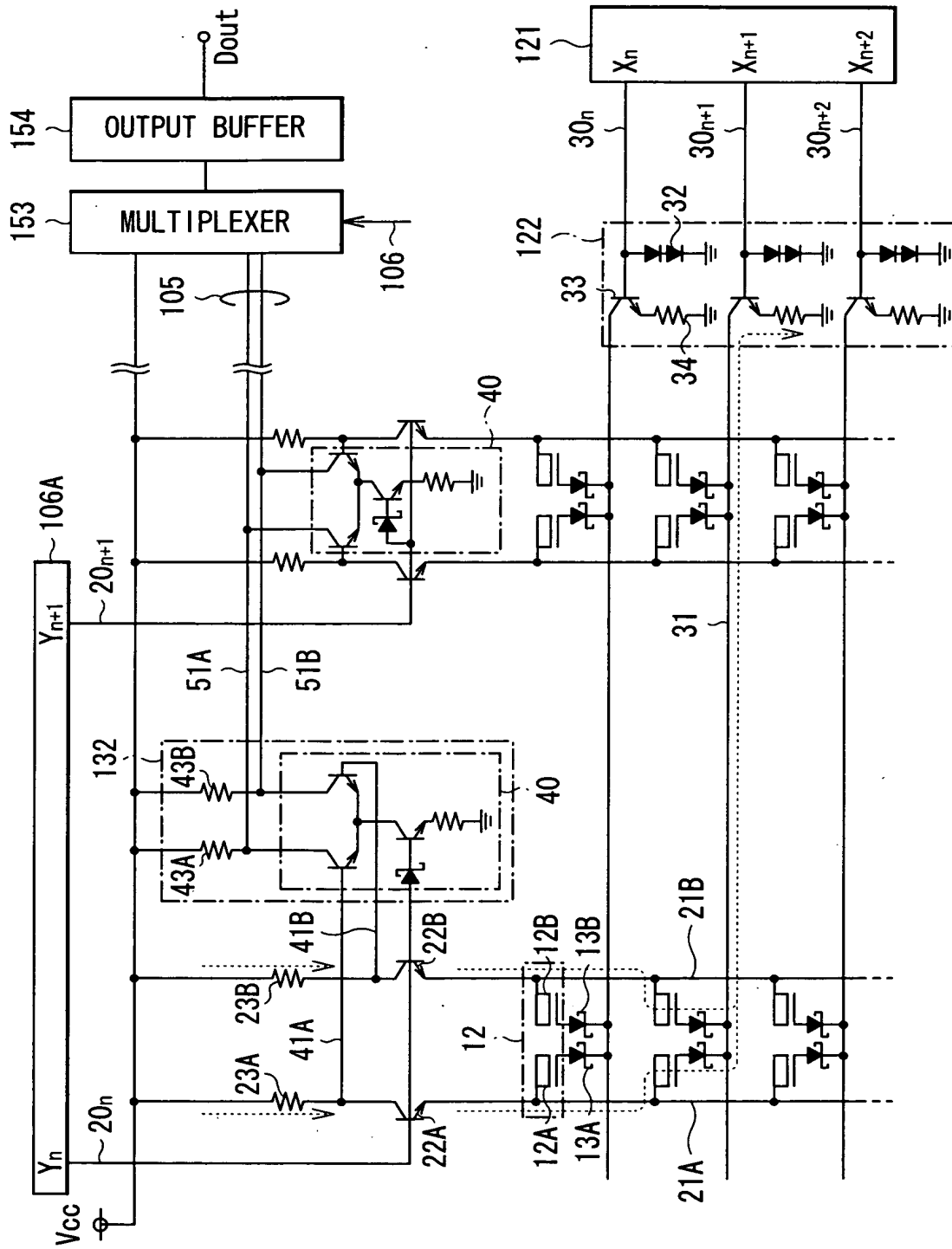


FIG. 17



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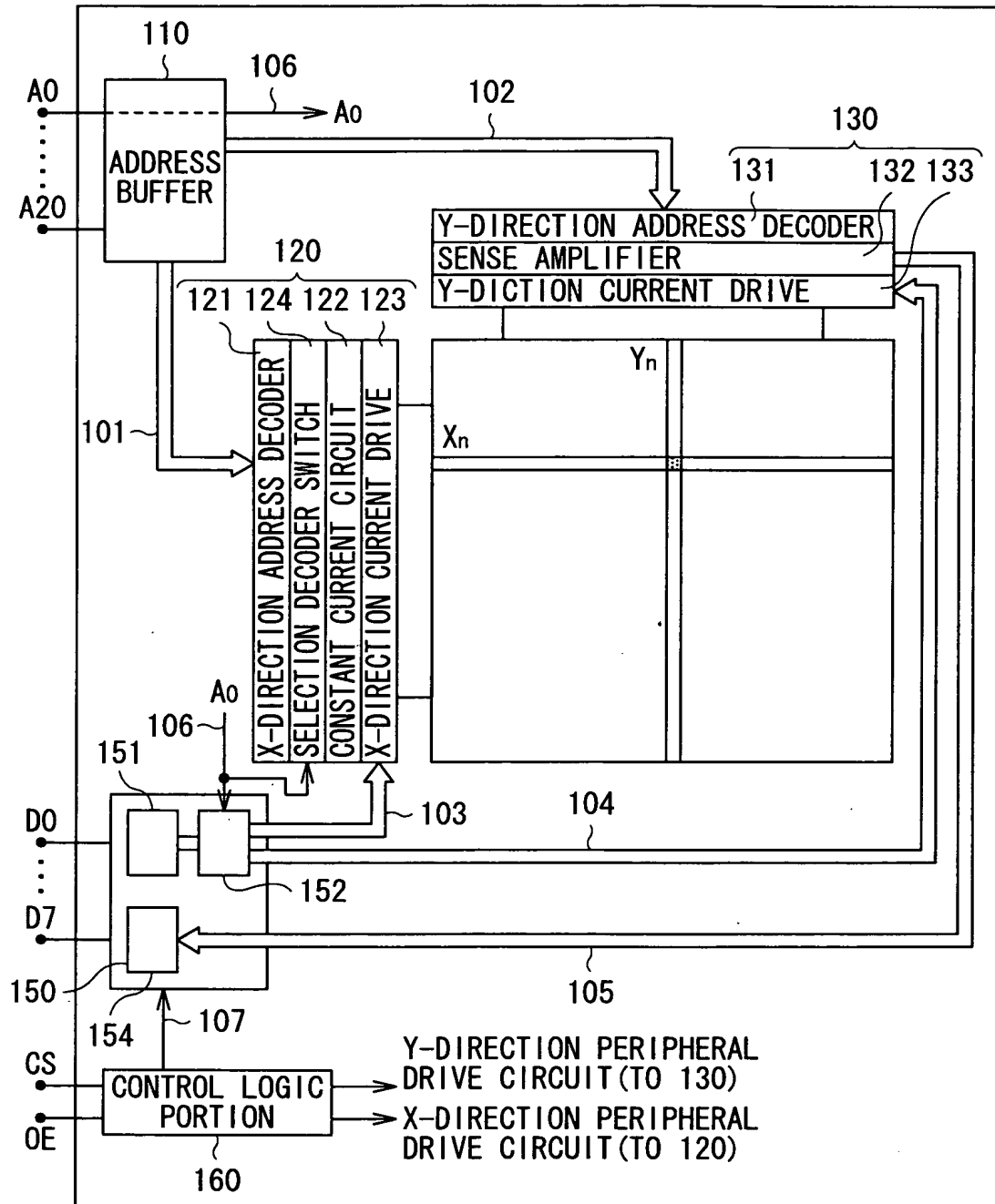


FIG. 18

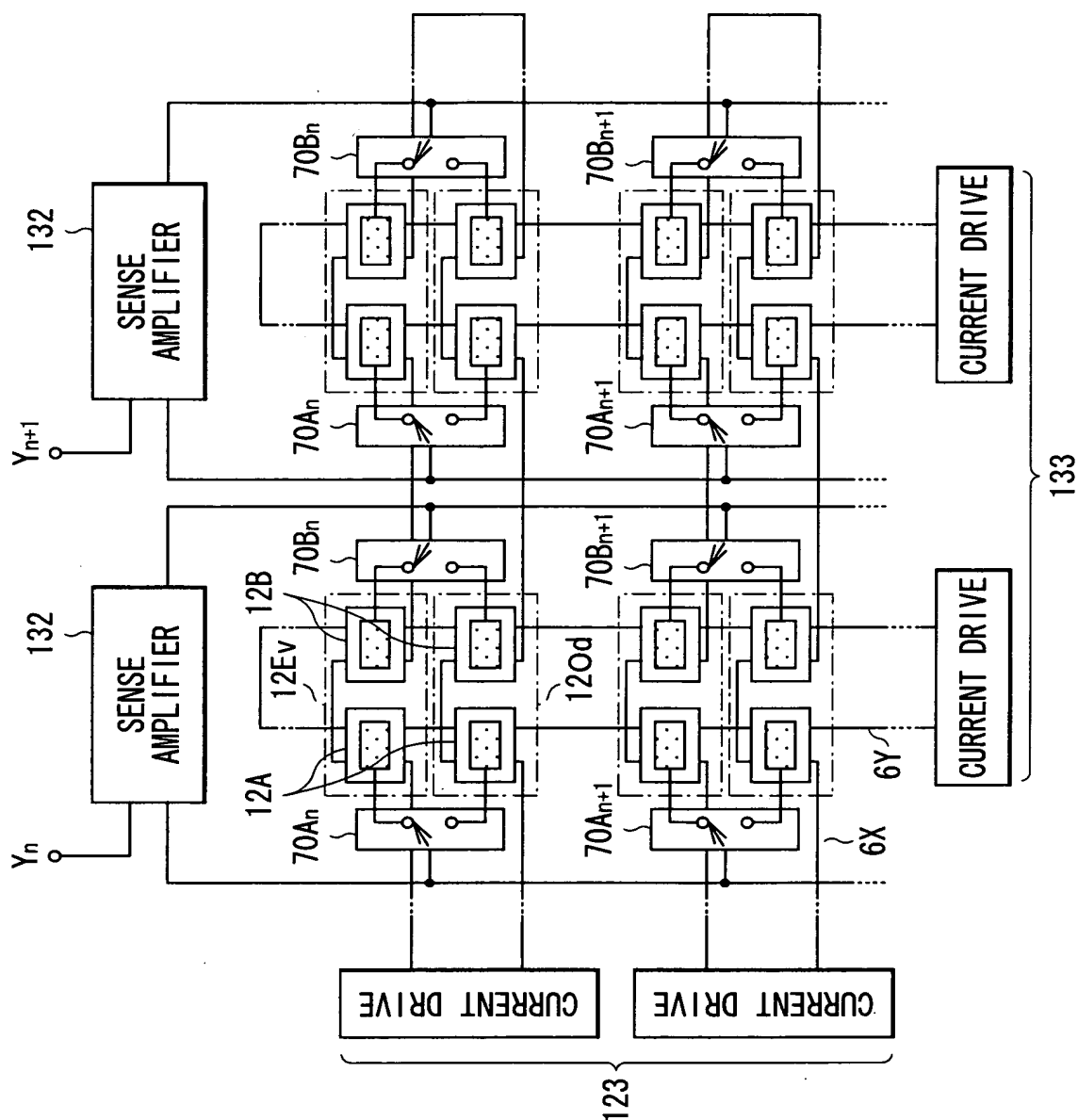


FIG. 19

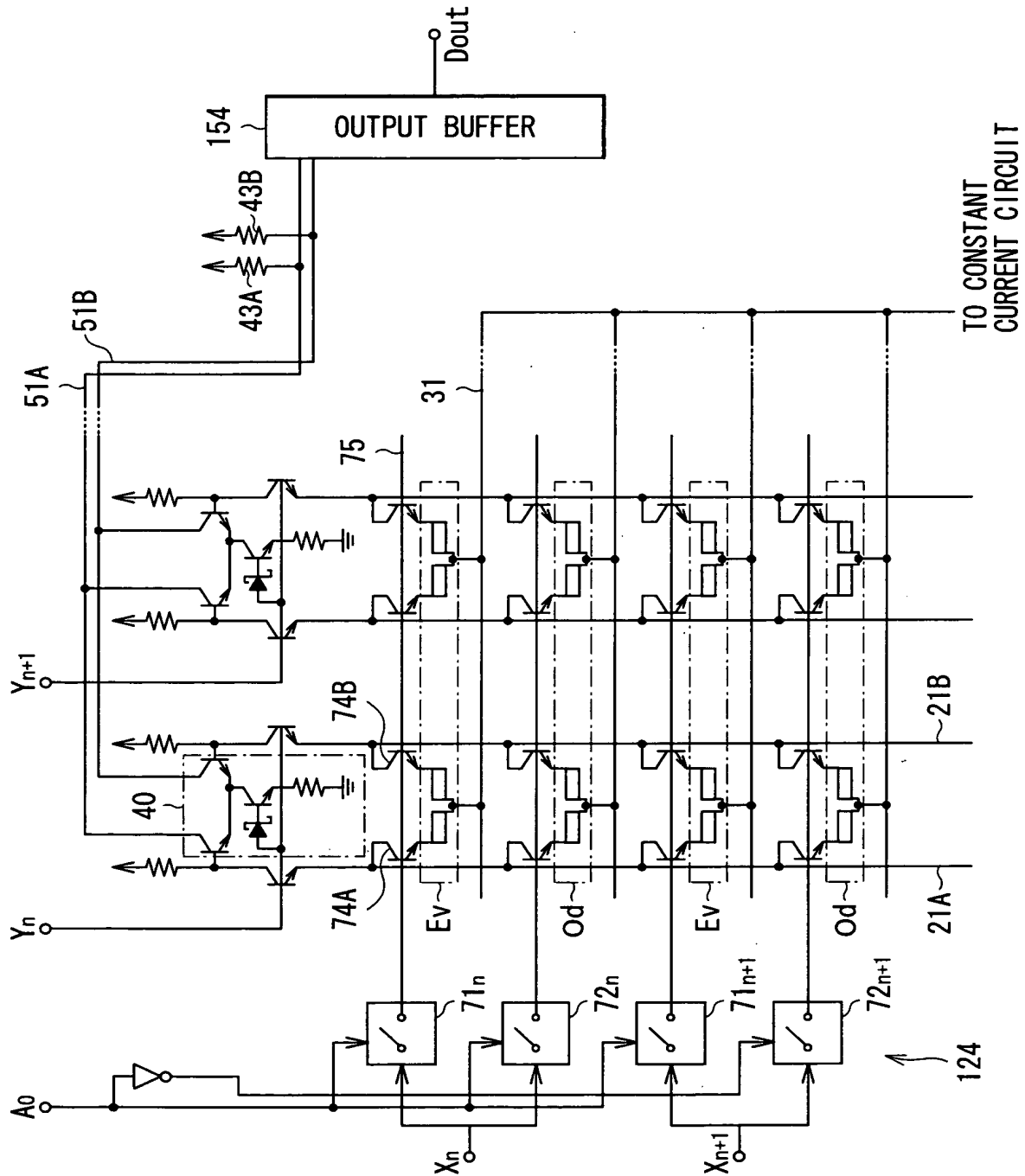


FIG. 20

FIG. 21A

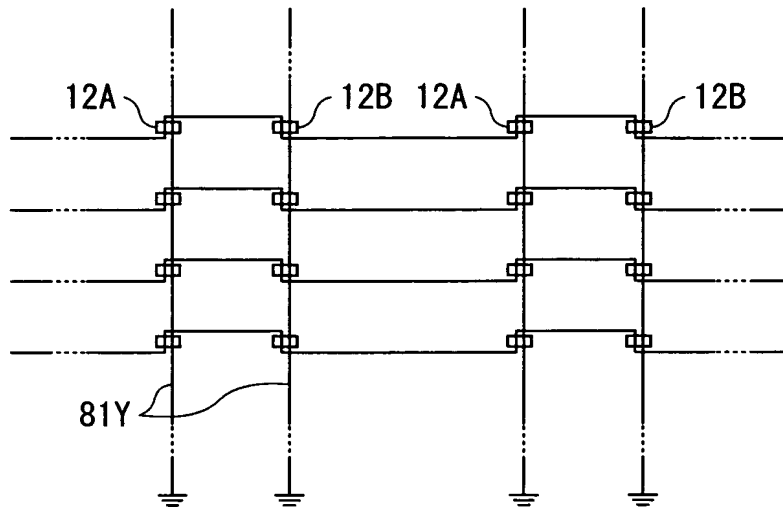


FIG. 21B

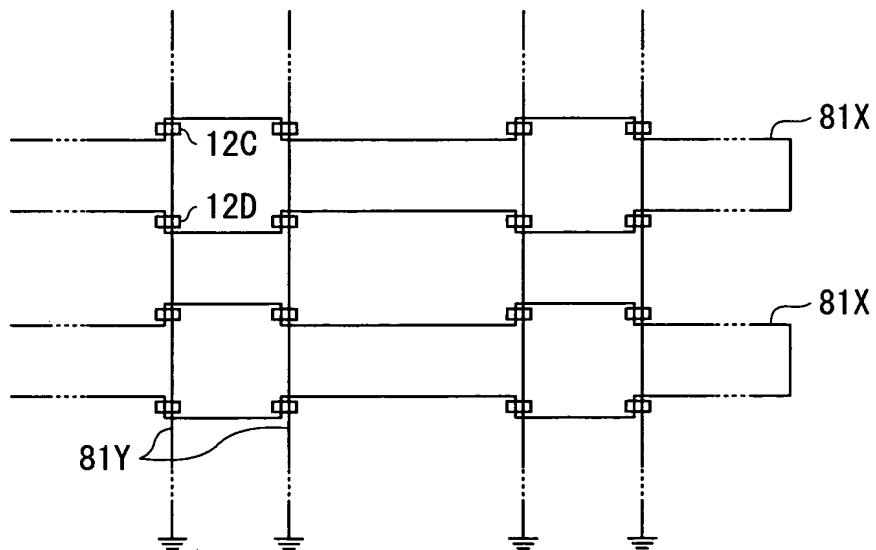


FIG. 22A

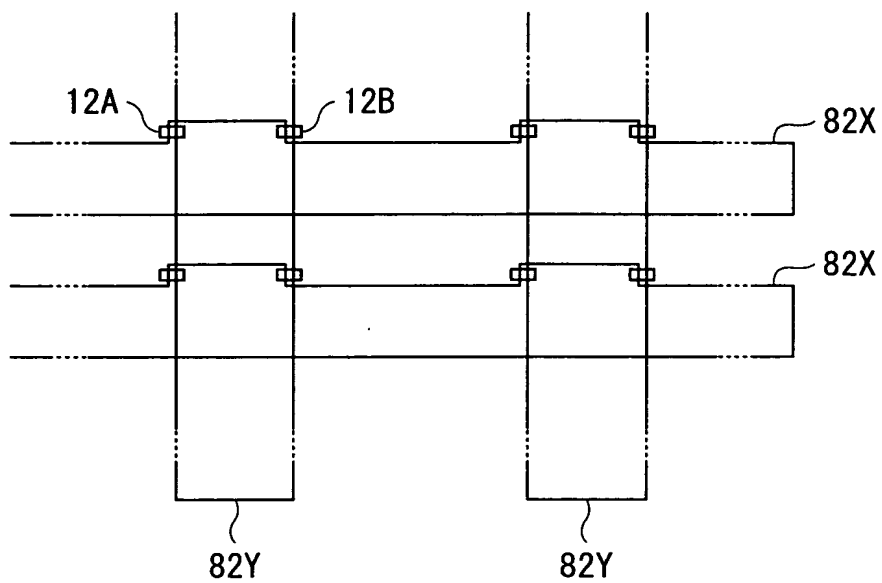
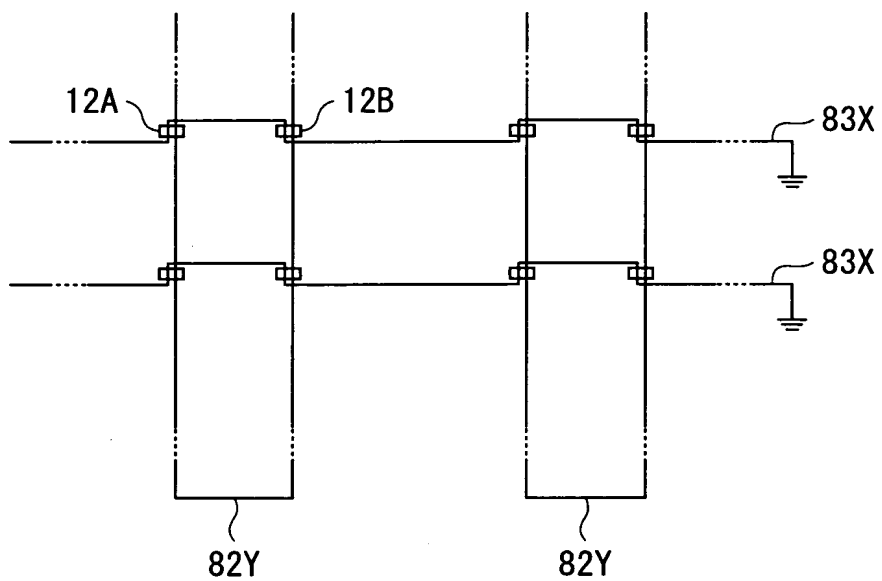


FIG. 22B



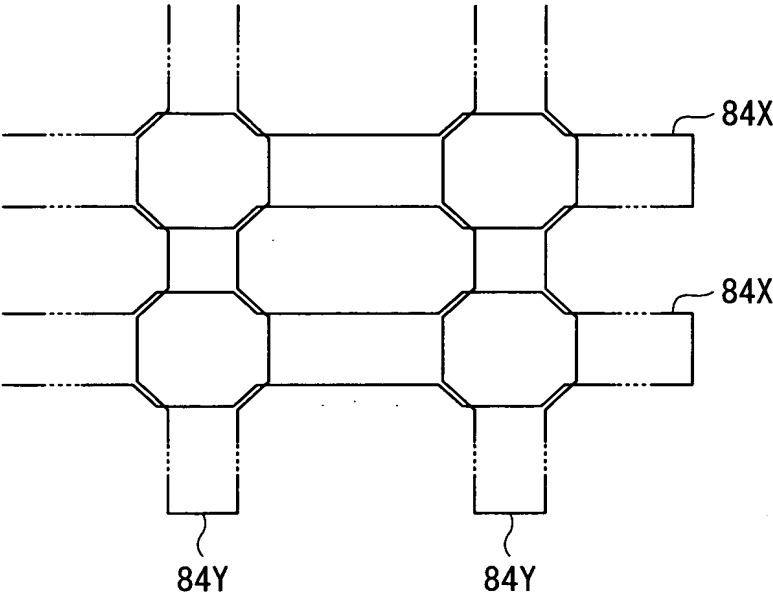


FIG. 23

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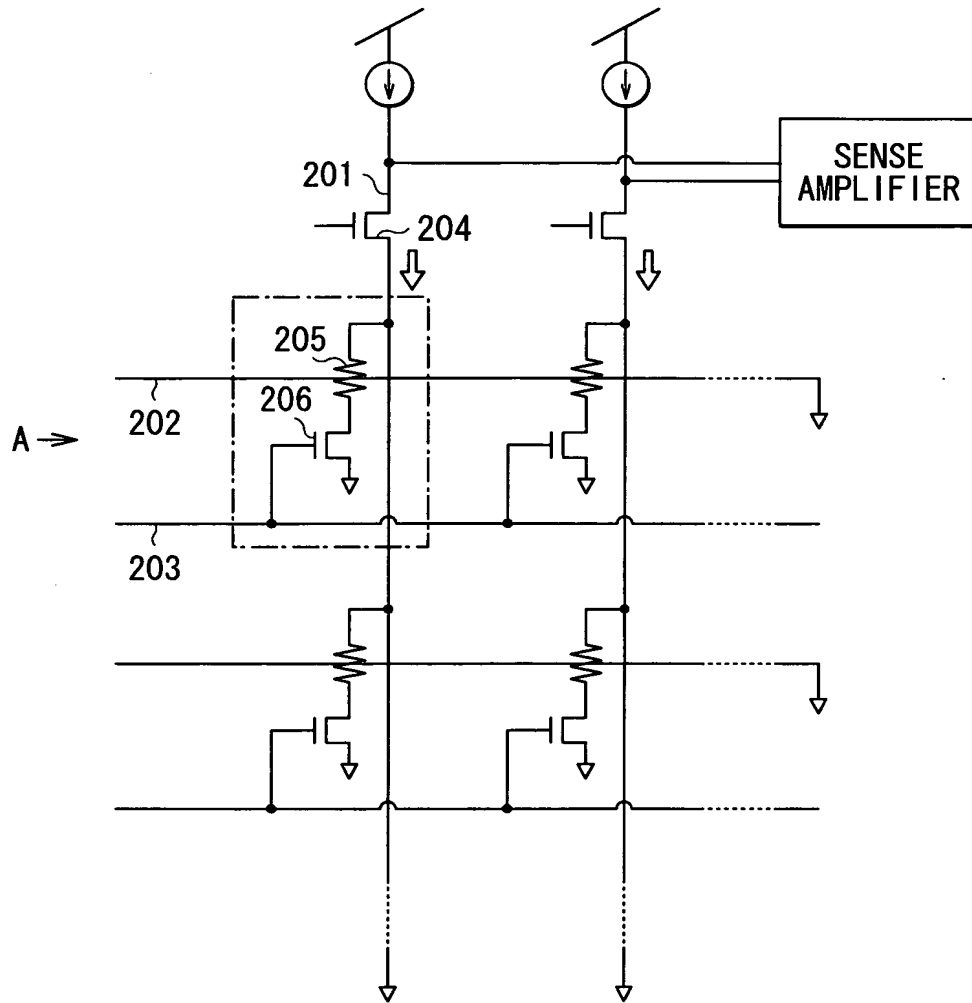


FIG. 24

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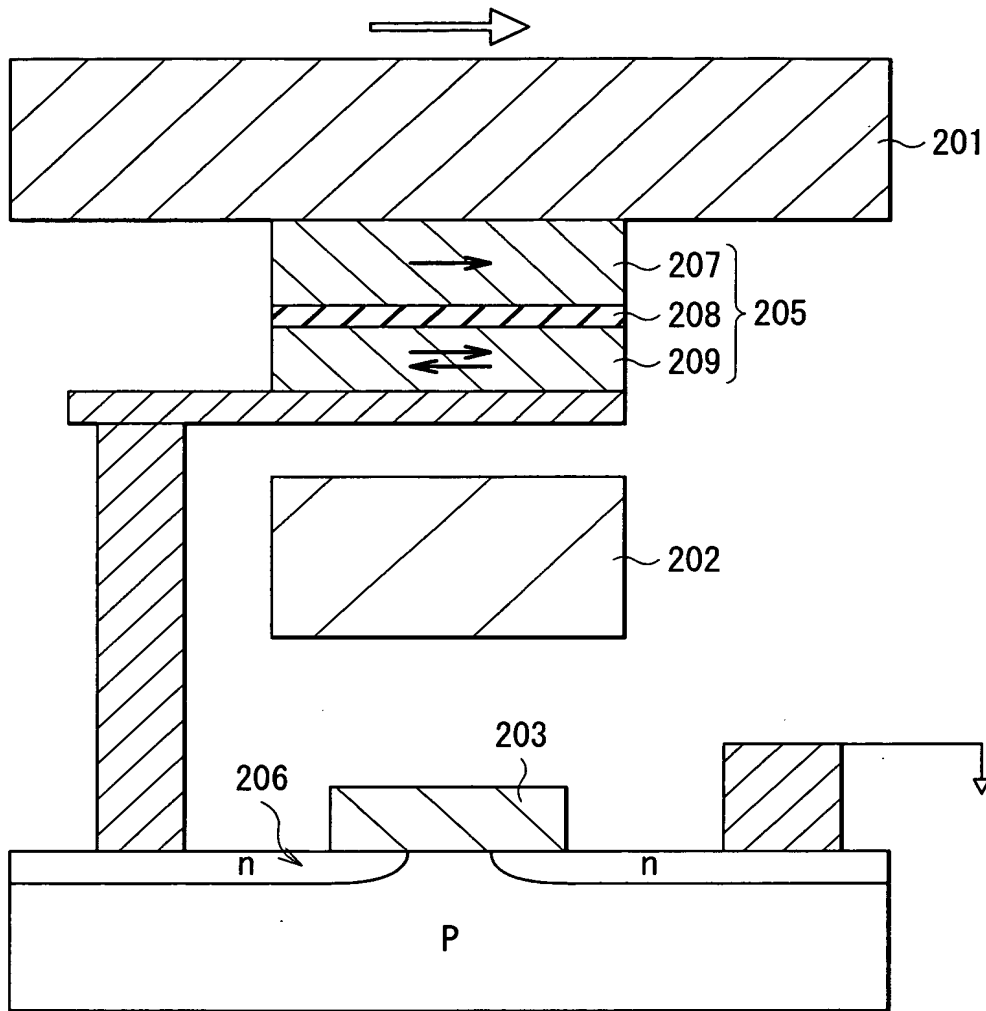


FIG. 25



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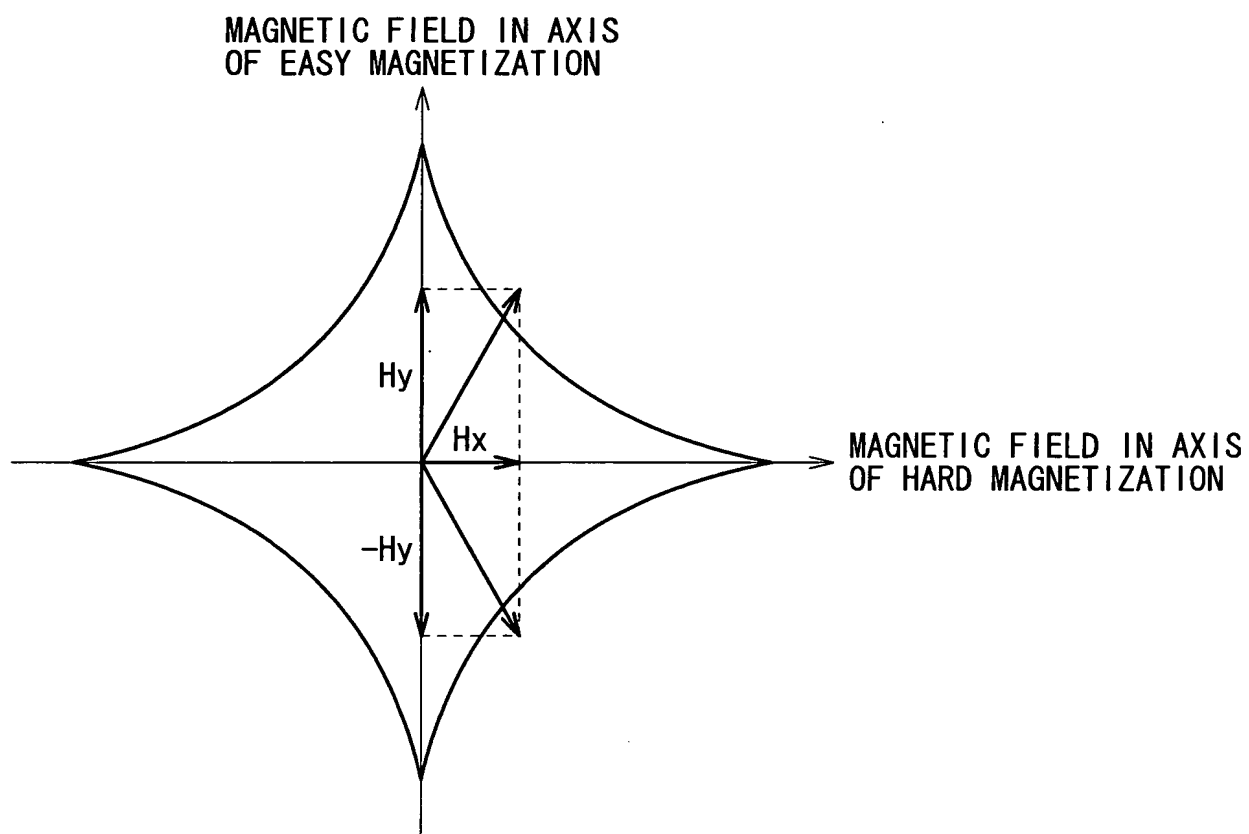


FIG. 26

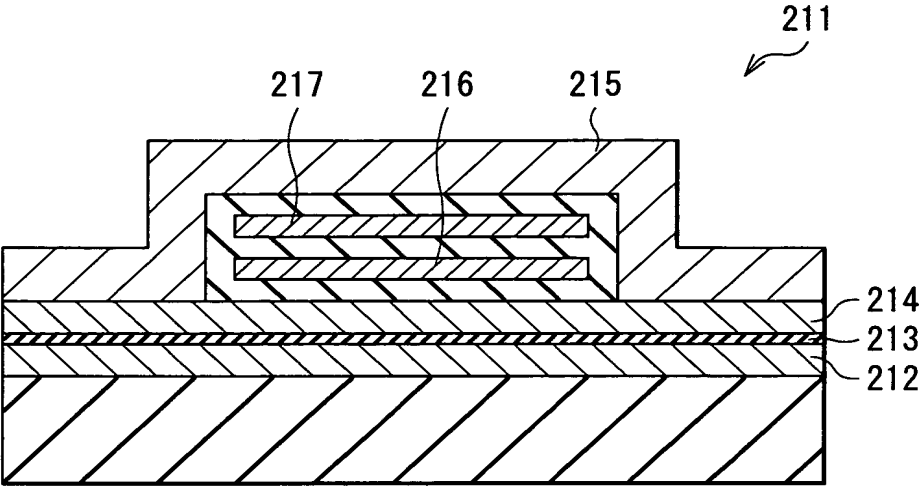


FIG. 27

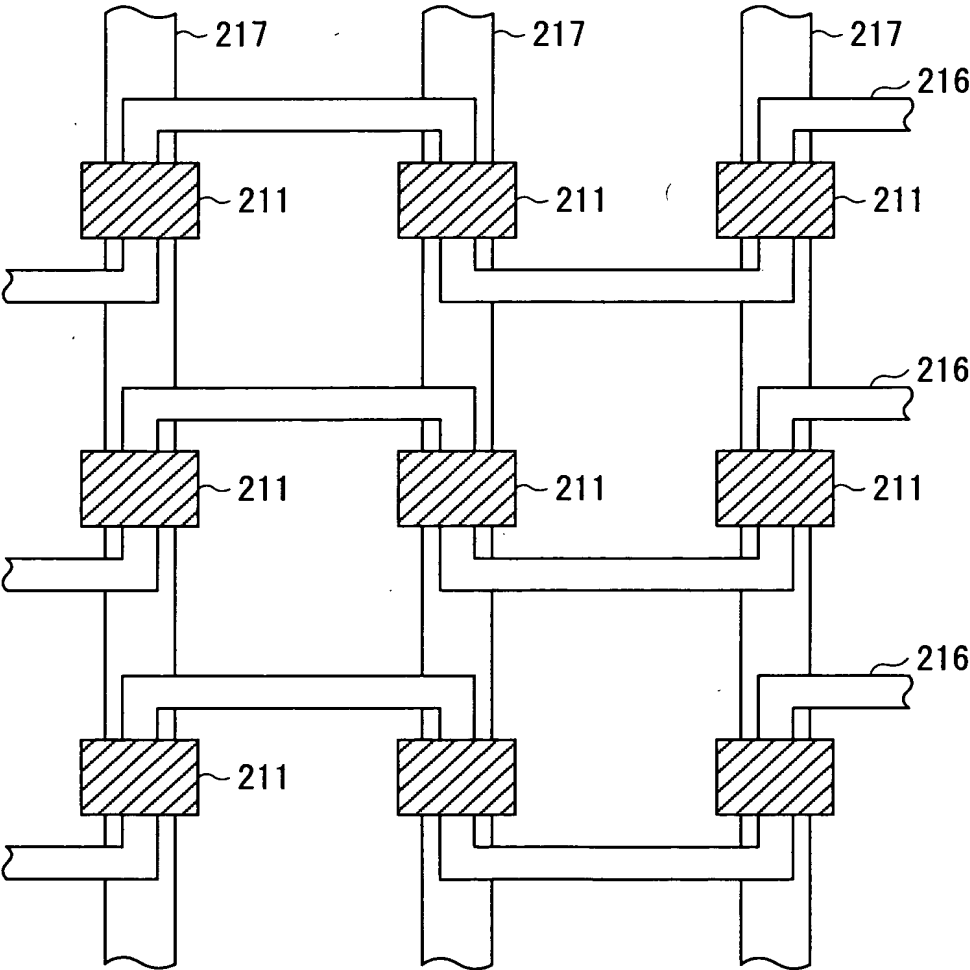


FIG. 28

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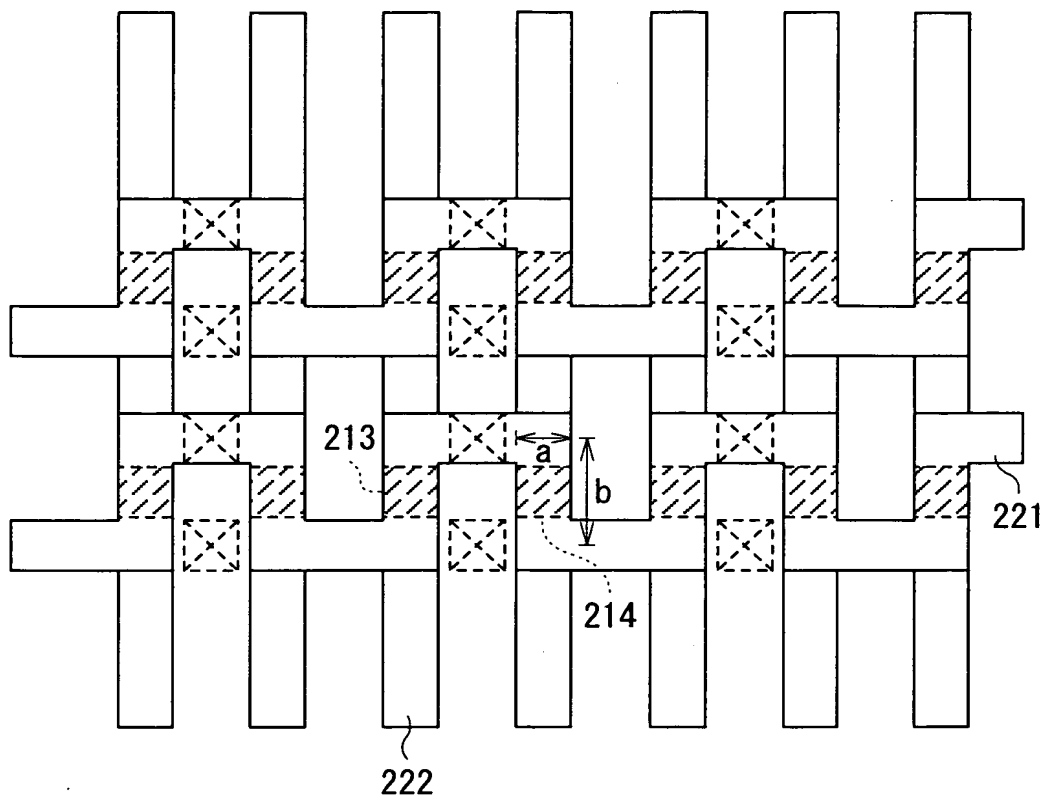


FIG. 29

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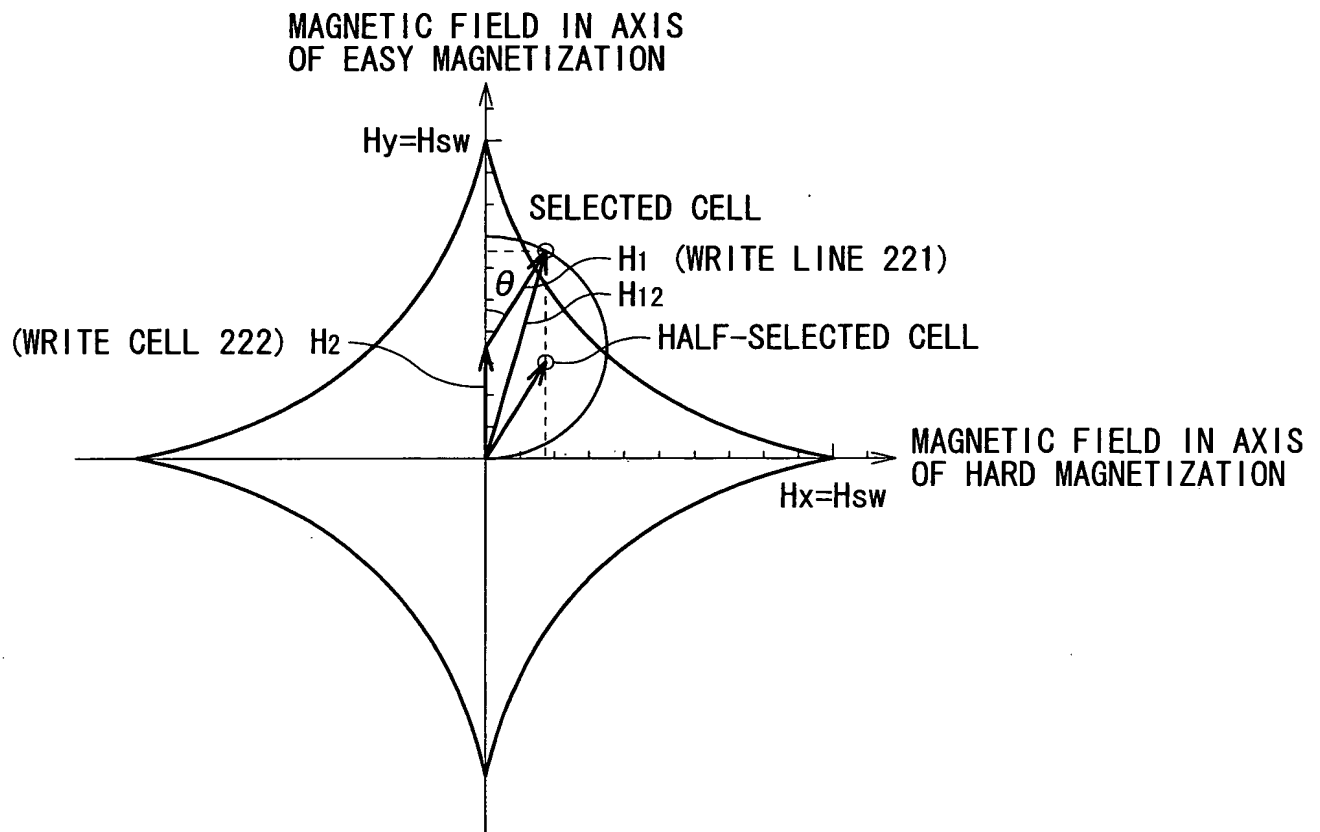


FIG. 30

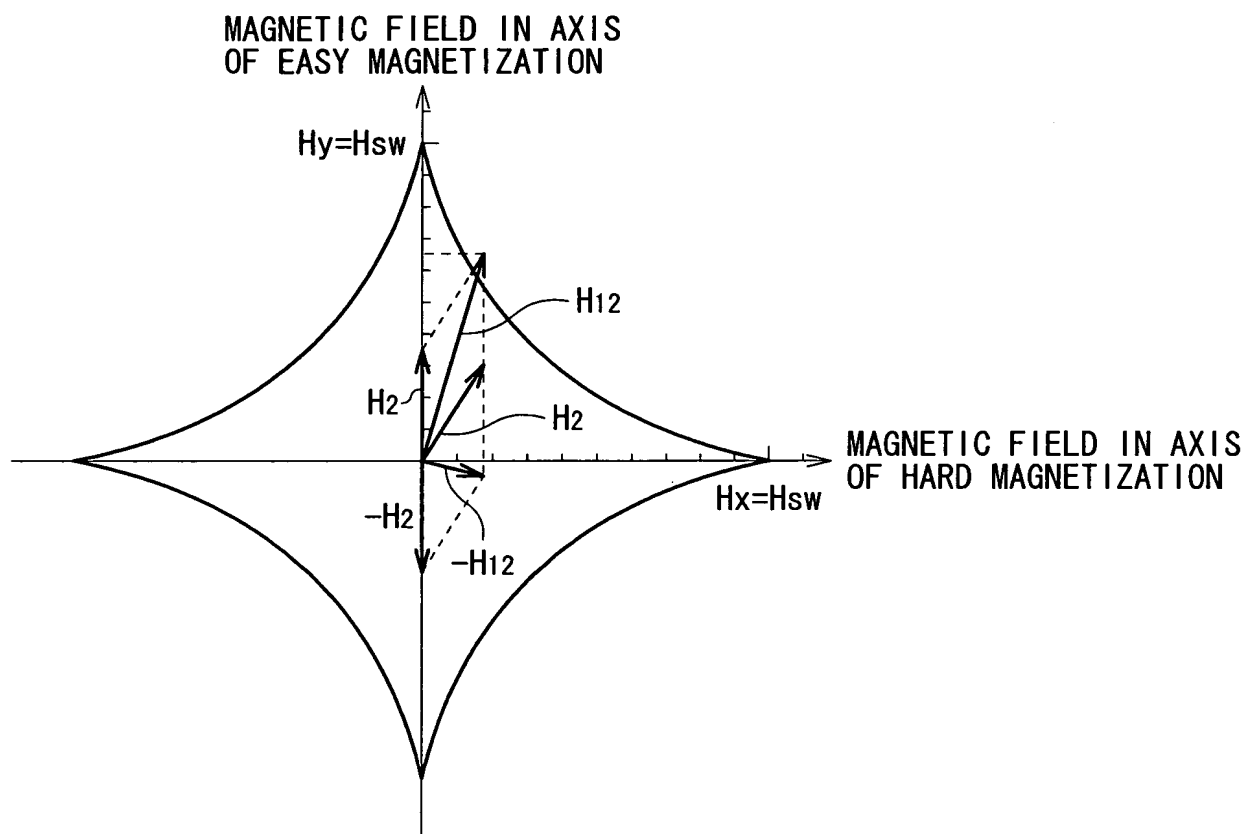


FIG. 31